

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): K. KURODA et al
Serial No.: (New Continuation Application Under 37 CFR §1.53(b) of
U.S. Application Serial No. 10/300,813)
Filed: October 28, 2003
For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE
Group: 2824
Examiner: V. LE

INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR §1.97 & §1.98

MS: PATENT APPLICATIONS

Commissioner For Patents
POB 1450
Alexandria, VA 22313-1450

October 28, 2003

Sir:

In the matter of the above-identified application, applicants are attaching hereto, in a form equivalent to Form PTO-1449, a listing of all the art documents of record (cited by the Examiner/submitted by applicants) in the chain of continuing applications (as listed on page 1 of the Specification), pursuant to the duty of disclosure/candor requirements.

This Information Disclosure Statement is being submitted concurrently with the filing of the above-identified continuation application and, therefore, this IDS meets the requirements pursuant to 37 CFR §1.97(b).

As to the requirement of 37 CFR §1.98(a)(3) for a concise explanation of relevance to the extent that any of the listed documents are not in the English language, this is met by: the discussion thereof in the file histories of the prior

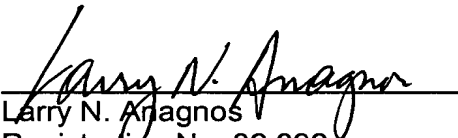
applications and in the original Specification, the Submission of English translation(s) and the citation thereof by the Examiner in the prior application(s).

It is understood that since copies of the listed documents should be available in the file wrapper of the prior application, copies thereof are not included herewith, consistent with 37 CFR §1.98(d) and pursuant to the guidelines set forth in MPEP §609.

It is submitted this IDS is in compliance with the rules of practice as well as with USPTO guidelines. Therefore, applicants respectfully request that it be entered and duly considered by the USPTO. Also, acknowledgment of entry as well as of formal consideration thereof by the Examiner is respectfully requested.

If any costs are due in accordance with the filing of this Information Disclosure Statement, please charge same to the account of Antonelli, Terry, Stout & Kraus, LLP, Account No. 01-2135 (501.30278CC6).

Respectfully submitted,
ANTONELLI, TERRY, STOUT & KRAUS, LLP


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Form PTO-1449	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DKT. NO. 501.30278CC6	SERIAL NO. R53(b) Cont. of 10/300,813
		INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)	
		APPLICANT K. Kuroda, et al. FILING DATE October 28, 2003 GROUP 2824	

U.S. PATENT DOCUMENTS

Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date
	A	5,179,536	01/12/93	Kasa et al.	365	200	11/20/91
	B	4,680,698	07/14/87	Edwards et al.	364	200	11/16/83
	C	4,704,678	11/03/87	May	364	200	11/16/83
	D	4,724,517	02/09/88	May	364	200	11/16/83
	E	4,819,151	04/04/89	May	364	200	11/20/86
	F	4,967,326	10/30/90	May	364	220	12/09/86
	G	5,031,092	07/09/91	Edwards et al.	364	200	07/14/89
	H	5,243,698	09/07/93	May	395	200	10/30/90
	I	5,452,467	09/19/95	May et al.	395	800	05/24/93
	J	5,491,359	02/13/96	May et al.	257	373	05/24/93
	K	5,506,437	04/09/96	May et al.	257	373	05/24/93
	L	4,939,386	7/90	Shibata et al.	307	304	
	M	4,918,501	4/90	Komori et al.	357	23.5	
	N	4,970,565	11/90	Wu et al.	257	318	9/88
	O	4,942,450	7/90	Iwashita	257	328	7/88
	P	4,866,493	9/89	Arima et al.	257	318	
	Q	4,019,197	4/77	Lohstroh et al.	257	318	
	R	5,767,544	6/16/98	Kuroda et al.	365	200X	6/6/95
	S	5,457,335	10/10/95	Kuroda et al.	257	318	7/9/91
	T	5,416,347	5/16/95	Katto et al.	257	315X	10/13/92
	U	4,731,642	3/15/88	Katto et al.	365	144X	
	V	4,988,637	1/21/91	Dhong et al	437	38	

FOREIGN PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation	
						Yes	No
	W	2-201800	8/90	Japan (corresponds to USP 5,179,536)		Abstract	
	X	59-194256	11/84	Japan (corresponds to USP 5,506,437)			
	Y	1-293537	11/89	Japan		Abstract	
	Z	60-83349	5/85	Japan			
	AA	61-124113	08-05-1999	Japan		X	
	Other Documents (including Author, Title, Date Pertinent pages, etc.)						
	AU	IEEE International Solid-State Circuits Conference, A Nonvolatile Memories@, Y. Naruke, et al., Digest of Technical Papers, Feb. 1989, pp. 128-129 and 311.					
	AV	International Electron Device Meeting, AA High Performance CMOS Technology for 256K/1MB EPROMs@, G. Gerosa, et al., 1985, pp. 631-634.					
	AW	The Transactions of the Institute of Electronics, Information, and Communication Engineers, Vol. 90, No. 47, May 21, 1990, "A Partially Programmable ROM", Y. Kasa, et al., pp.51-53.					
Examiner				Date Considered			